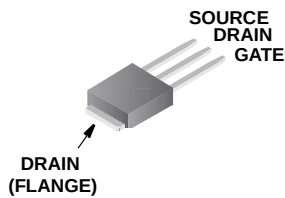


**20A, 100V, 0.054 Ohm, N-Channel, Logic  
Level UltraFET® Power MOSFET**

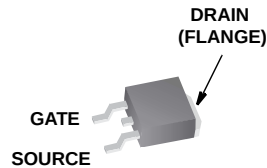
**Packaging**

JEDEC TO-251AA

JEDEC TO-252AA

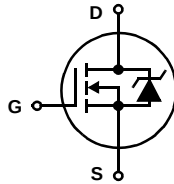


**HUF76629D3**



**HUF76629D3S**

**Symbol**



**Features**

- Ultra Low On-Resistance
  - $r_{DS(ON)} = 0.052\Omega$ ,  $V_{GS} = 10V$
  - $r_{DS(ON)} = 0.054\Omega$ ,  $V_{GS} = 5V$
- Simulation Models
  - Temperature Compensated PSPICE® and SABER™ Electrical Models
  - Spice and SABER Thermal Impedance Models
  - [www.fairchildsemi.com](http://www.fairchildsemi.com)
- Peak Current vs Pulse Width Curve
- UIS Rating Curve
- Switching Time vs  $R_{GS}$  Curves

**Ordering Information**

| PART NUMBER | PACKAGE  | BRAND  |
|-------------|----------|--------|
| HUF76629D3  | TO-251AA | 76629D |
| HUF76629D3S | TO-252AA | 76629D |

NOTE: When ordering, use the entire part number. Add the suffix T to obtain the variant in tape and reel, e.g., HUF76629D3ST.

**Absolute Maximum Ratings**  $T_C = 25^\circ\text{C}$ , Unless Otherwise Specified

|                                                                                 | HUF76629D3, HUF76629D3S   | UNITS               |
|---------------------------------------------------------------------------------|---------------------------|---------------------|
| Drain to Source Voltage (Note 1) . . . . .                                      | $V_{DSS}$ 100             | V                   |
| Drain to Gate Voltage ( $R_{GS} = 20k\Omega$ ) (Note 1) . . . . .               | $V_{DGR}$ 100             | V                   |
| Gate to Source Voltage . . . . .                                                | $V_{GS}$ $\pm 16$         | V                   |
| Drain Current                                                                   |                           |                     |
| Continuous ( $T_C = 25^\circ\text{C}$ , $V_{GS} = 5V$ ) . . . . .               | $I_D$ 20                  | A                   |
| Continuous ( $T_C = 25^\circ\text{C}$ , $V_{GS} = 10V$ ) (Figure 2) . . . . .   | $I_D$ 20                  | A                   |
| Continuous ( $T_C = 100^\circ\text{C}$ , $V_{GS} = 5V$ ) . . . . .              | $I_D$ 20                  | A                   |
| Continuous ( $T_C = 100^\circ\text{C}$ , $V_{GS} = 4.5V$ ) (Figure 2) . . . . . | $I_D$ 20                  | A                   |
| Pulsed Drain Current . . . . .                                                  | $I_{DM}$ Figure 4         |                     |
| Pulsed Avalanche Rating . . . . .                                               | UIS Figures 6, 17, 18     |                     |
| Power Dissipation . . . . .                                                     | $P_D$ 110                 | W                   |
| Derate Above $25^\circ\text{C}$ . . . . .                                       | 0.74                      | W/ $^\circ\text{C}$ |
| Operating and Storage Temperature . . . . .                                     | $T_J, T_{STG}$ -55 to 175 | $^\circ\text{C}$    |
| Maximum Temperature for Soldering                                               |                           |                     |
| Leads at 0.063in (1.6mm) from Case for 10s. . . . .                             | $T_L$ 300                 | $^\circ\text{C}$    |
| Package Body for 10s, See Techbrief TB334. . . . .                              | $T_{pkg}$ 260             | $^\circ\text{C}$    |

**NOTES:**

1.  $T_J = 25^\circ\text{C}$  to  $150^\circ\text{C}$ .

**CAUTION:** Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Product reliability information can be found at <http://www.fairchildsemi.com/products/discrete/reliability/index.html>

For severe environments, see our Automotive HUFA series.

All Fairchild semiconductor products are manufactured, assembled and tested under ISO9000 and QS9000 quality systems certification.

# HUF76629D3, HUF76629D3S

## Electrical Specifications $T_C = 25^{\circ}\text{C}$ , Unless Otherwise Specified

| PARAMETER                                         | SYMBOL              | TEST CONDITIONS                                                                                                       | MIN                                                                                                    | TYP    | MAX   | UNITS |    |
|---------------------------------------------------|---------------------|-----------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------|--------|-------|-------|----|
| OFF STATE SPECIFICATIONS                          |                     |                                                                                                                       |                                                                                                        |        |       |       |    |
| Drain to Source Breakdown Voltage                 | BV <sub>DSS</sub>   | I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V (Figure 12)                                                              | 100                                                                                                    | -      | -     | V     |    |
|                                                   |                     | I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V , T <sub>C</sub> = -40°C (Figure 12)                                     | 90                                                                                                     | -      | -     | V     |    |
| Zero Gate Voltage Drain Current                   | I <sub>DSS</sub>    | V <sub>DS</sub> = 95V, V <sub>GS</sub> = 0V                                                                           | -                                                                                                      | -      | 1     | μA    |    |
|                                                   |                     | V <sub>DS</sub> = 90V, V <sub>GS</sub> = 0V, T <sub>C</sub> = 150°C                                                   | -                                                                                                      | -      | 250   | μA    |    |
| Gate to Source Leakage Current                    | I <sub>GSS</sub>    | V <sub>GS</sub> = ±16V                                                                                                | -                                                                                                      | -      | ±100  | nA    |    |
| ON STATE SPECIFICATIONS                           |                     |                                                                                                                       |                                                                                                        |        |       |       |    |
| Gate to Source Threshold Voltage                  | V <sub>GS(TH)</sub> | V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 250μA (Figure 11)                                                | 1                                                                                                      | -      | 3     | V     |    |
| Drain to Source On Resistance                     | r <sub>DS(ON)</sub> | I <sub>D</sub> = 20A, V <sub>GS</sub> = 10V (Figures 9, 10)                                                           | -                                                                                                      | 0.0415 | 0.052 | Ω     |    |
|                                                   |                     | I <sub>D</sub> = 20A, V <sub>GS</sub> = 5V (Figure 9)                                                                 | -                                                                                                      | 0.046  | 0.054 | Ω     |    |
|                                                   |                     | I <sub>D</sub> = 20A, V <sub>GS</sub> = 4.5V (Figure 9)                                                               | -                                                                                                      | 0.047  | 0.055 | Ω     |    |
| THERMAL SPECIFICATIONS                            |                     |                                                                                                                       |                                                                                                        |        |       |       |    |
| Thermal Resistance Junction to Case               | R <sub>θJC</sub>    | TO-251AA and TO-252AA                                                                                                 | -                                                                                                      | -      | 1.36  | °C/W  |    |
| Thermal Resistance Junction to Ambient            | R <sub>θJA</sub>    |                                                                                                                       | -                                                                                                      | -      | 100   | °C/W  |    |
| SWITCHING SPECIFICATIONS (V <sub>GS</sub> = 4.5V) |                     |                                                                                                                       |                                                                                                        |        |       |       |    |
| Turn-On Time                                      | t <sub>ON</sub>     | V <sub>DD</sub> = 50V, I <sub>D</sub> = 20A<br>V <sub>GS</sub> = 4.5V, R <sub>GS</sub> = 6.8Ω<br>(Figures 15, 21, 22) | -                                                                                                      | -      | 190   | ns    |    |
| Turn-On Delay Time                                | t <sub>d(ON)</sub>  |                                                                                                                       | -                                                                                                      | 11     | -     | ns    |    |
| Rise Time                                         | t <sub>r</sub>      |                                                                                                                       | -                                                                                                      | 114    | -     | ns    |    |
| Turn-Off Delay Time                               | t <sub>d(OFF)</sub> |                                                                                                                       | -                                                                                                      | 38     | -     | ns    |    |
| Fall Time                                         | t <sub>f</sub>      |                                                                                                                       | -                                                                                                      | 60     | -     | ns    |    |
| Turn-Off Time                                     | t <sub>OFF</sub>    |                                                                                                                       | -                                                                                                      | -      | 145   | ns    |    |
| SWITCHING SPECIFICATIONS (V <sub>GS</sub> = 10V)  |                     |                                                                                                                       |                                                                                                        |        |       |       |    |
| Turn-On Time                                      | t <sub>ON</sub>     | V <sub>DD</sub> = 50V, I <sub>D</sub> = 20A<br>V <sub>GS</sub> = 10V,R <sub>GS</sub> = 8.2Ω<br>(Figures 16, 21, 22)   | -                                                                                                      | -      | 50    | ns    |    |
| Turn-On Delay Time                                | t <sub>d(ON)</sub>  |                                                                                                                       | -                                                                                                      | 6.8    | -     | ns    |    |
| Rise Time                                         | t <sub>r</sub>      |                                                                                                                       | -                                                                                                      | 28     | -     | ns    |    |
| Turn-Off Delay Time                               | t <sub>d(OFF)</sub> |                                                                                                                       | -                                                                                                      | 67     | -     | ns    |    |
| Fall Time                                         | t <sub>f</sub>      |                                                                                                                       | -                                                                                                      | 60     | -     | ns    |    |
| Turn-Off Time                                     | t <sub>OFF</sub>    |                                                                                                                       | -                                                                                                      | -      | 190   | ns    |    |
| GATE CHARGE SPECIFICATIONS                        |                     |                                                                                                                       |                                                                                                        |        |       |       |    |
| Total Gate Charge                                 | Q <sub>g(TOT)</sub> | V <sub>GS</sub> = 0V to 10V                                                                                           | V <sub>DD</sub> = 50V,<br>I <sub>D</sub> = 20A,<br>I <sub>g(REF)</sub> = 1.0mA<br>(Figures 14, 19, 20) | -      | 38    | 46    | nC |
| Gate Charge at 5V                                 | Q <sub>g(5)</sub>   | V <sub>GS</sub> = 0V to 5V                                                                                            |                                                                                                        | -      | 21    | 25    | nC |
| Threshold Gate Charge                             | Q <sub>g(TH)</sub>  | V <sub>GS</sub> = 0V to 1V                                                                                            |                                                                                                        | -      | 1.2   | 1.6   | nC |
| Gate to Source Gate Charge                        | Q <sub>gs</sub>     |                                                                                                                       |                                                                                                        | -      | 3.3   | -     | nC |
| Gate to Drain "Miller" Charge                     | Q <sub>gd</sub>     |                                                                                                                       |                                                                                                        | -      | 10    | -     | nC |
| CAPACITANCE SPECIFICATIONS                        |                     |                                                                                                                       |                                                                                                        |        |       |       |    |
| Input Capacitance                                 | C <sub>ISS</sub>    | V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V,<br>f = 1MHz<br>(Figure 13)                                               | -                                                                                                      | 1285   | -     | pF    |    |
| Output Capacitance                                | C <sub>OSS</sub>    |                                                                                                                       | -                                                                                                      | 270    | -     | pF    |    |
| Reverse Transfer Capacitance                      | C <sub>RSS</sub>    |                                                                                                                       | -                                                                                                      | 65     | -     | pF    |    |

## Source to Drain Diode Specifications

| PARAMETER                     | SYMBOL   | TEST CONDITIONS                                                | MIN | TYP | MAX  | UNITS |
|-------------------------------|----------|----------------------------------------------------------------|-----|-----|------|-------|
| Source to Drain Diode Voltage | $V_{SD}$ | $I_{SD} = 20\text{A}$                                          | -   | -   | 1.25 | V     |
|                               |          | $I_{SD} = 10\text{A}$                                          | -   | -   | 1.00 | V     |
| Reverse Recovery Time         | $t_{rr}$ | $I_{SD} = 20\text{A}$ , $dI_{SD}/dt = 100\text{A}/\mu\text{s}$ | -   | -   | 110  | ns    |
| Reverse Recovered Charge      | $Q_{RR}$ | $I_{SD} = 20\text{A}$ , $dI_{SD}/dt = 100\text{A}/\mu\text{s}$ | -   | -   | 370  | nC    |

# Typical Performance Curves

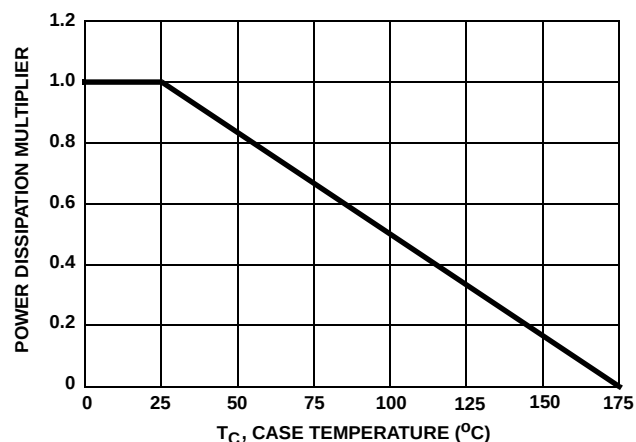


FIGURE 1. NORMALIZED POWER DISSIPATION vs CASE TEMPERATURE

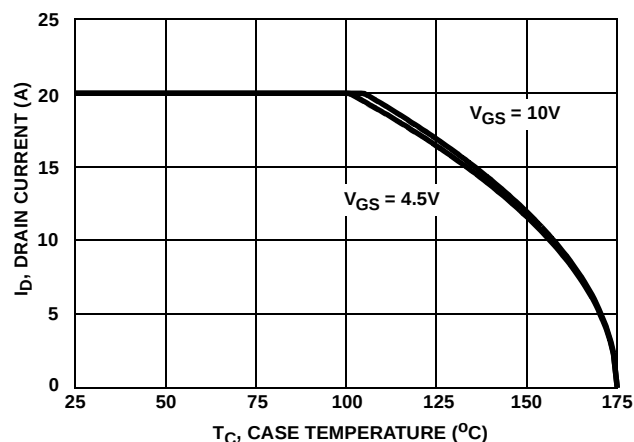


FIGURE 2. MAXIMUM CONTINUOUS DRAIN CURRENT vs CASE TEMPERATURE

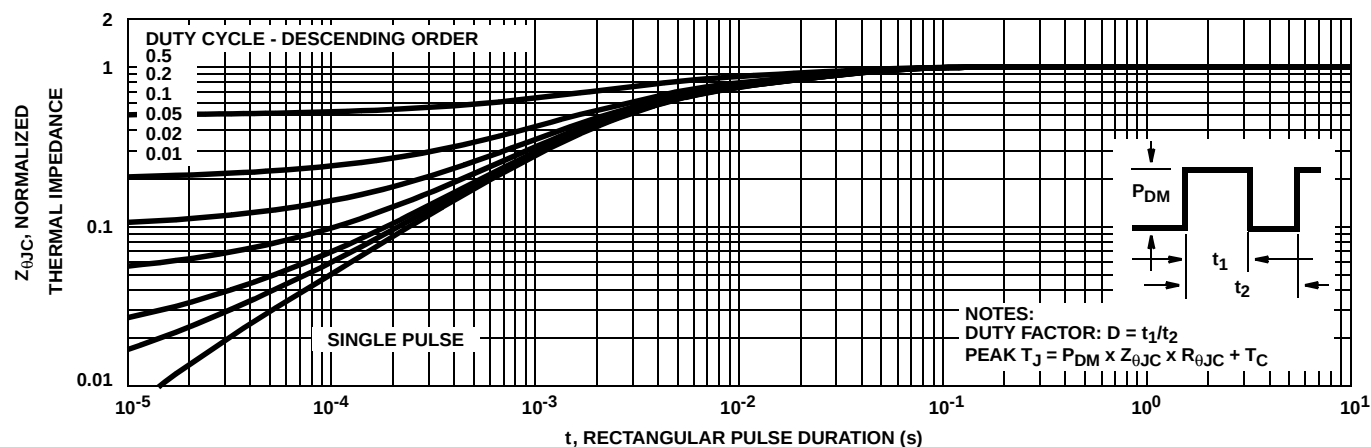


FIGURE 3. NORMALIZED MAXIMUM TRANSIENT THERMAL IMPEDANCE

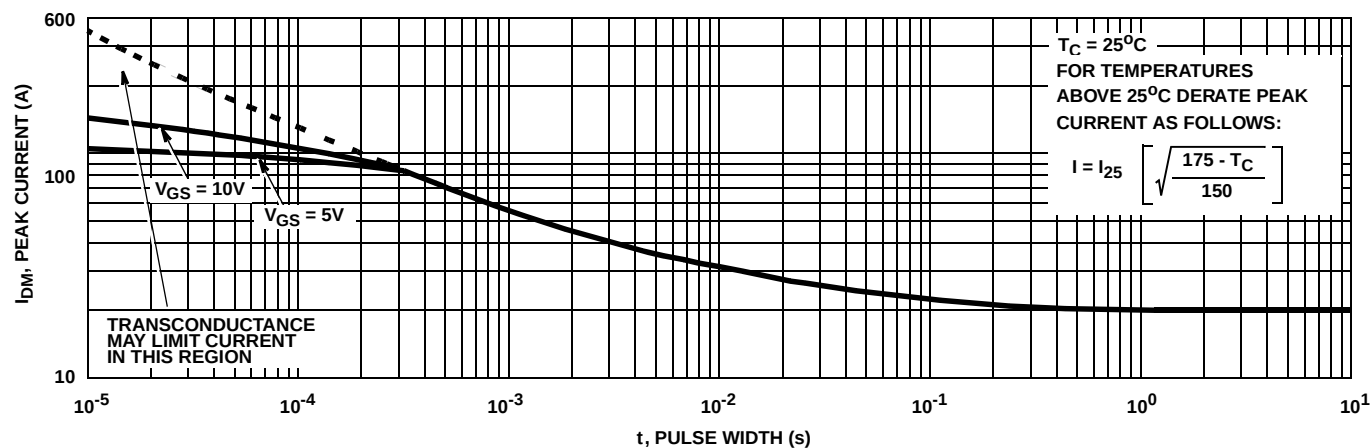


FIGURE 4. PEAK CURRENT CAPABILITY

Typical Performance Curves (Continued)

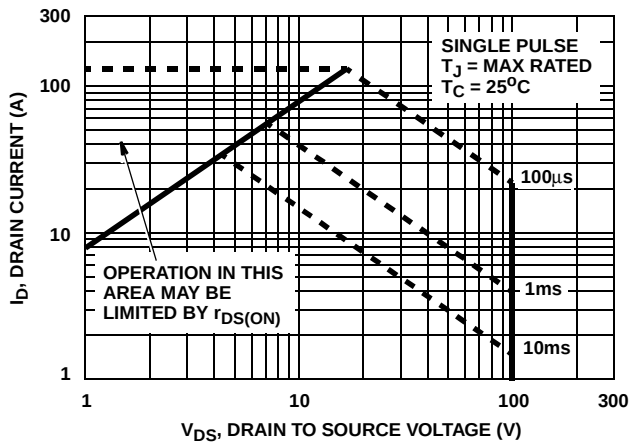
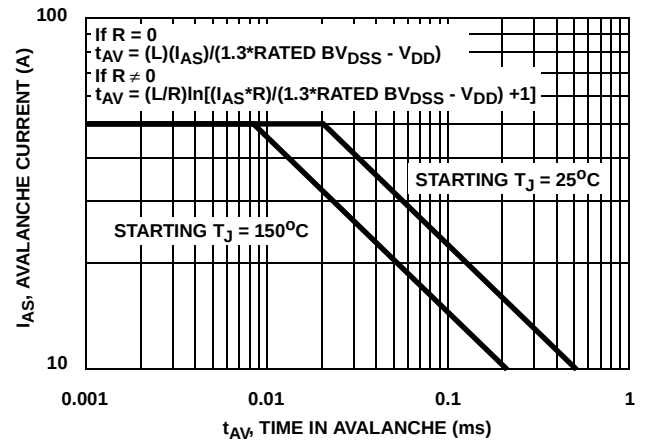


FIGURE 5. FORWARD BIAS SAFE OPERATING AREA



NOTE: Refer to Fairchild Application Notes AN9321 and AN9322.

FIGURE 6. UNCLAMPED INDUCTIVE SWITCHING CAPABILITY

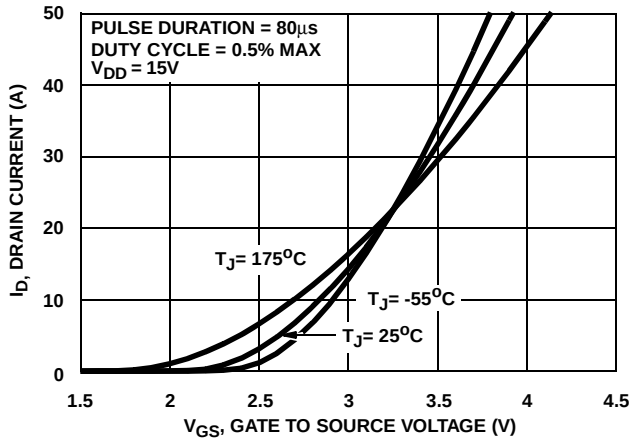


FIGURE 7. TRANSFER CHARACTERISTICS

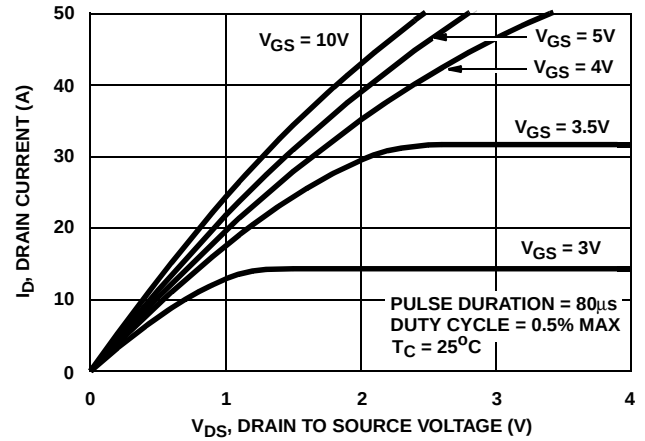


FIGURE 8. SATURATION CHARACTERISTICS

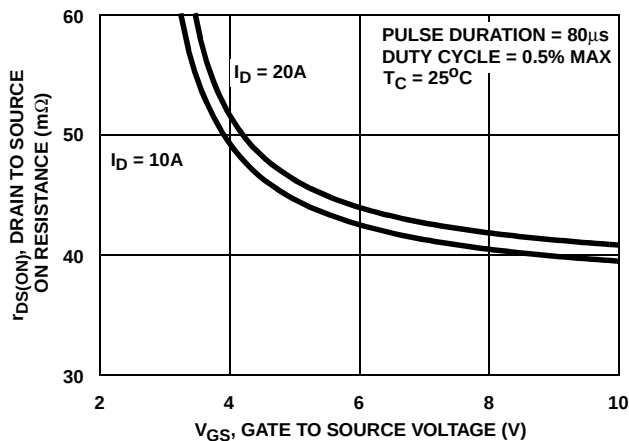


FIGURE 9. DRAIN TO SOURCE ON RESISTANCE vs GATE VOLTAGE AND DRAIN CURRENT

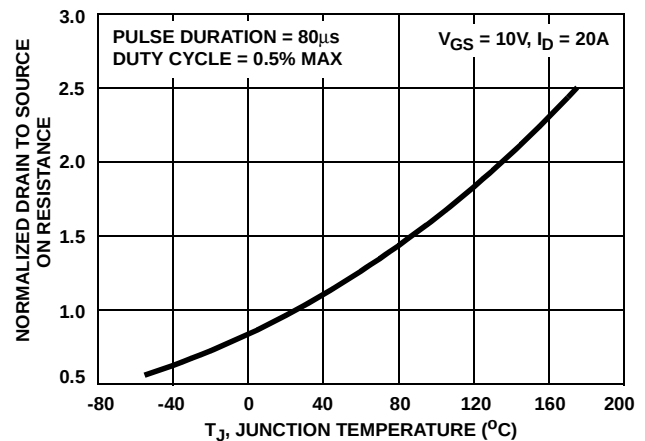


FIGURE 10. NORMALIZED DRAIN TO SOURCE ON RESISTANCE vs JUNCTION TEMPERATURE

Typical Performance Curves (Continued)

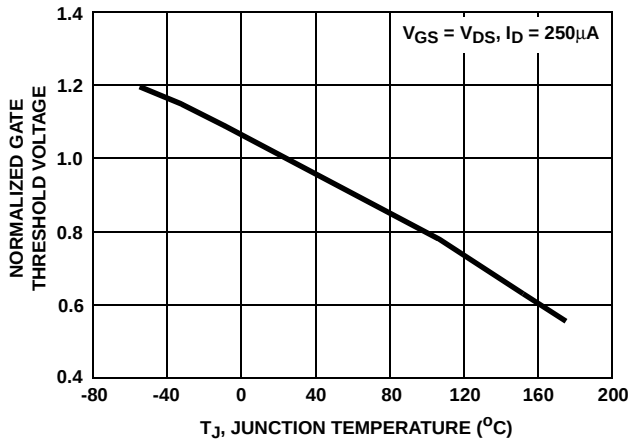


FIGURE 11. NORMALIZED GATE THRESHOLD VOLTAGE vs JUNCTION TEMPERATURE

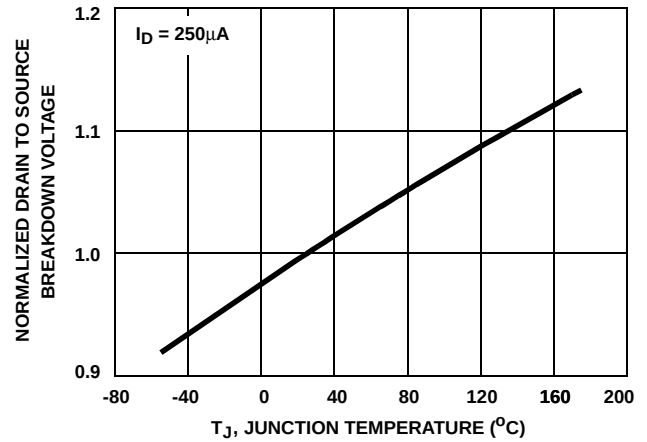


FIGURE 12. NORMALIZED DRAIN TO SOURCE BREAKDOWN VOLTAGE vs JUNCTION TEMPERATURE

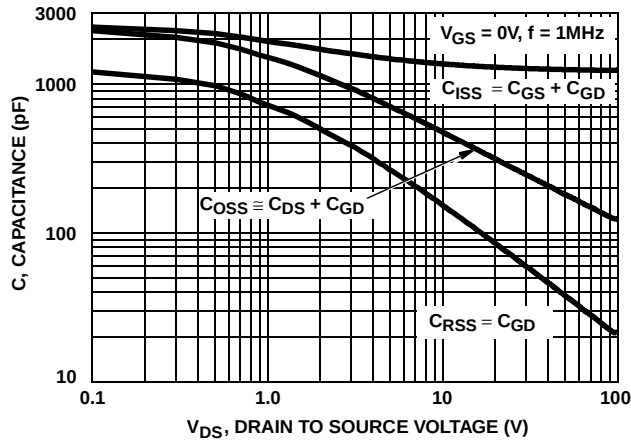
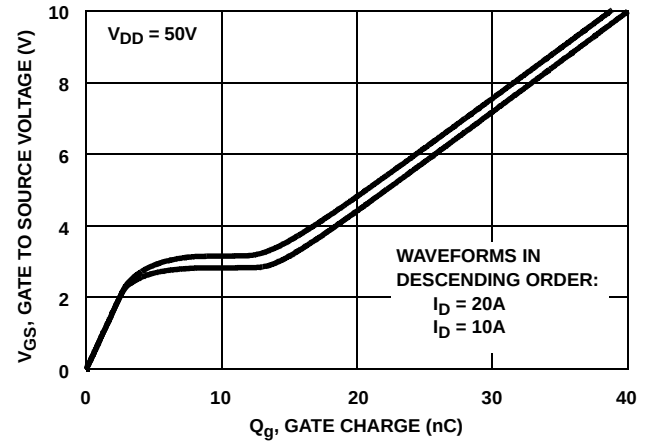


FIGURE 13. CAPACITANCE vs DRAIN TO SOURCE VOLTAGE



NOTE: Refer to Fairchild Application Notes AN7254 and AN7260.

FIGURE 14. GATE CHARGE WAVEFORMS FOR CONSTANT GATE CURRENT

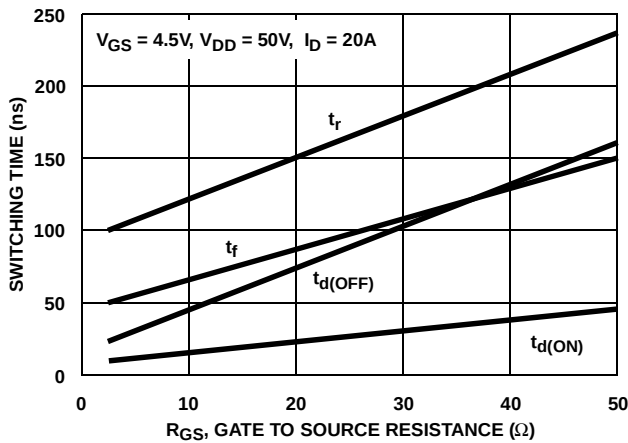


FIGURE 15. SWITCHING TIME vs GATE RESISTANCE

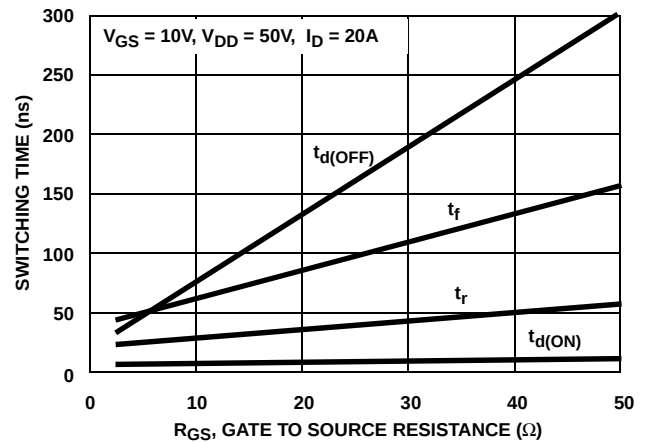


FIGURE 16. SWITCHING TIME vs GATE RESISTANCE

# Test Circuits and Waveforms

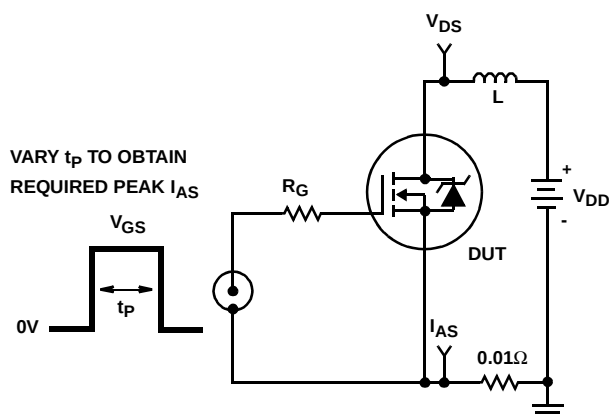


FIGURE 17. UNCLAMPED ENERGY TEST CIRCUIT

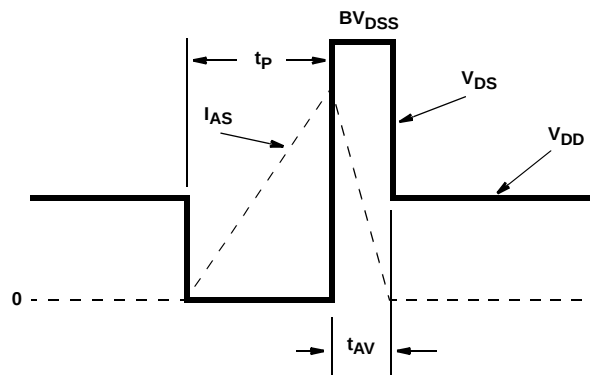


FIGURE 18. UNCLAMPED ENERGY WAVEFORMS

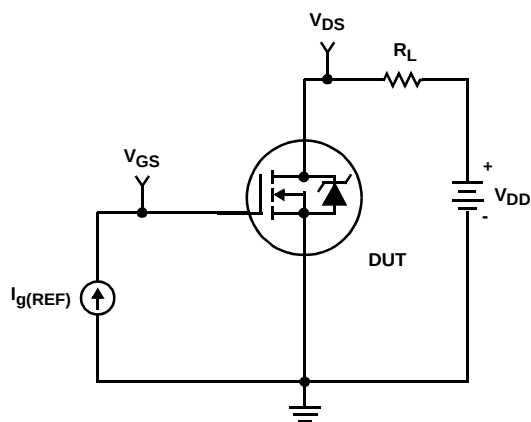


FIGURE 19. GATE CHARGE TEST CIRCUIT

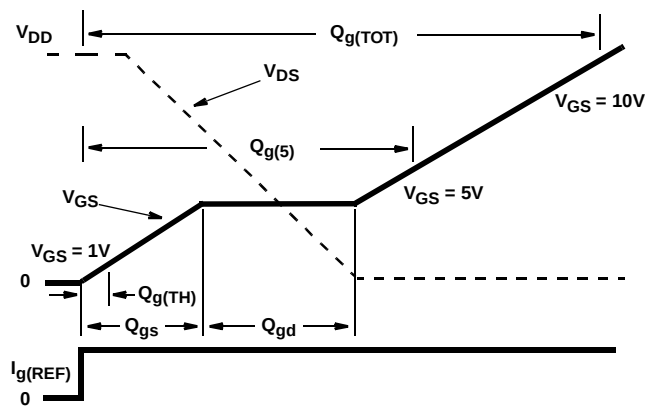


FIGURE 20. GATE CHARGE WAVEFORMS

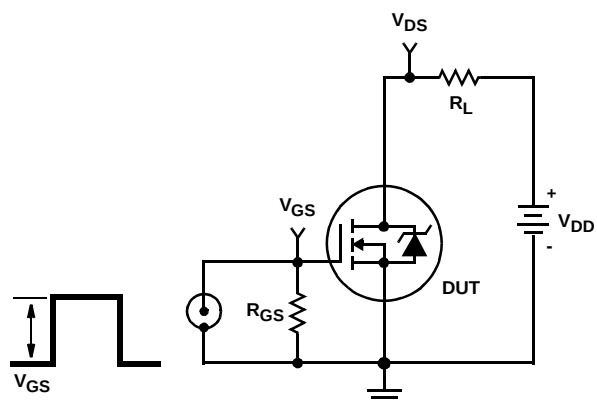


FIGURE 21. SWITCHING TIME TEST CIRCUIT

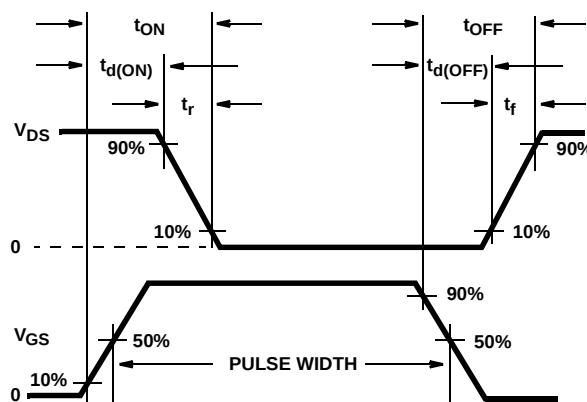


FIGURE 22. SWITCHING TIME WAVEFORM





## SPICE Thermal Model

REV 26 July 1999

HUF76629D3

CTHERM1 th 6 2.45e-3  
 CTHERM2 6 5 8.15e-3  
 CTHERM3 5 4 7.40e-3  
 CTHERM4 4 3 7.45e-3  
 CTHERM5 3 2 1.01e-2  
 CTHERM6 2 tl 7.49e-2

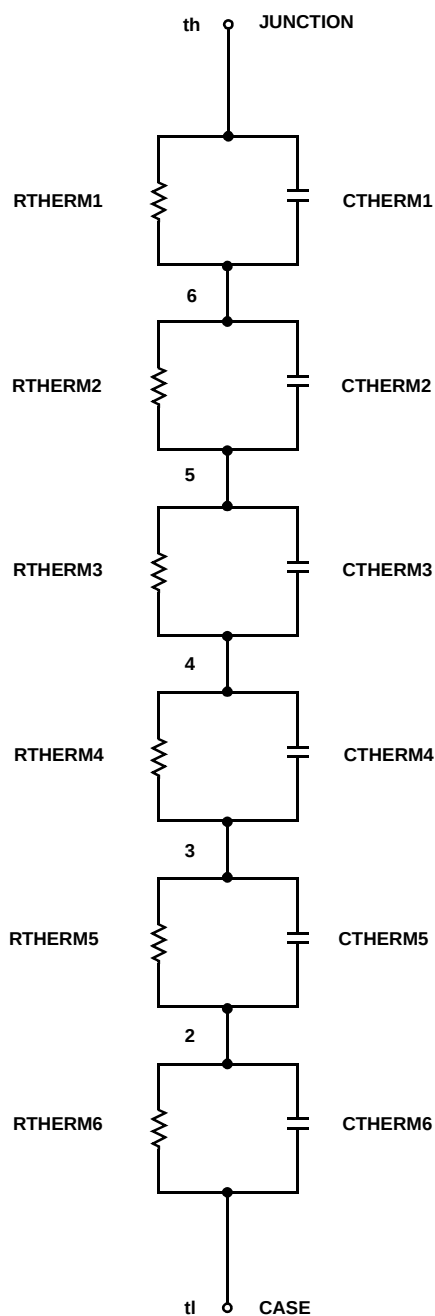
RTHERM1 th 6 9.00e-3  
 RTHERM2 6 5 1.80e-2  
 RTHERM3 5 4 9.15e-2  
 RTHERM4 4 3 2.43e-1  
 RTHERM5 3 2 3.50e-1  
 RTHERM6 2 tl 3.62e-1

## SABER Thermal Model

SABER thermal model HUF76629D3

```
template thermal_model th tl
thermal_c th, tl
{
    ctherm.ctherm1 th 6 = 2.45e-3
    ctherm.ctherm2 6 5 = 8.15e-3
    ctherm.ctherm3 5 4 = 7.40e-3
    ctherm.ctherm4 4 3 = 7.45e-3
    ctherm.ctherm5 3 2 = 1.01e-2
    ctherm.ctherm6 2 tl = 7.49e-2
```

```
    rtherm.rtherm1 th 6 = 9.00e-3
    rtherm.rtherm2 6 5 = 1.80e-2
    rtherm.rtherm3 5 4 = 9.15e-2
    rtherm.rtherm4 4 3 = 2.43e-1
    rtherm.rtherm5 3 2 = 3.50e-1
    rtherm.rtherm6 2 tl = 3.62e-1
}
```



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|                      |                     |                              |                       |      |
|----------------------|---------------------|------------------------------|-----------------------|------|
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| Bottomless™          | FAST <sup>r</sup> ™ | OPTOPLANAR™                  | STAR*POWER™           |      |
| CoolFET™             | FRFET™              | PACMAN™                      | Stealth™              |      |
| CROSSVOLT™           | GlobalOptoisolator™ | POP™                         | SuperSOT™-3           |      |
| DenseTrench™         | GTO™                | Power247™                    | SuperSOT™-6           |      |
| DOME™                | HiSeC™              | PowerTrench <sup>®</sup>     | SuperSOT™-8           |      |
| EcoSPARK™            | ISOPLANAR™          | QFET™                        | SyncFET™              |      |
| E <sup>2</sup> CMOS™ | LittleFET™          | QS™                          | TinyLogic™            |      |
| EnSigna™             | MicroFET™           | QT Optoelectronics™          | TruTranslation™       |      |
| FACT™                | MicroPak™           | Quiet Series™                | UHC™                  |      |
| FACT Quiet Series™   | MICROWIRE™          | SILENT SWITCHER <sup>®</sup> | UltraFET <sup>®</sup> |      |

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2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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| Datasheet Identification | Product Status         | Definition                                                                                                                                                                                                            |
|--------------------------|------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Advance Information      | Formative or In Design | This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.                                                                                    |
| Preliminary              | First Production       | This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| No Identification Needed | Full Production        | This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.                                                       |
| Obsolete                 | Not In Production      | This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.                                                   |



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Благодаря развитой сети поставщиков, помогаем в поиске и приобретении экзотичных или снятых с производства компонентов.

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- Гарантия качества поставляемой продукции
- Широкий ассортимент
- Минимальные сроки поставок
- Техническая поддержка
- Подбор комплектации
- Индивидуальный подход
- Гибкое ценообразование
- Работаем по 275 ФЗ