

FDD6676

30V N-Channel PowerTrench® MOSFET

General Description

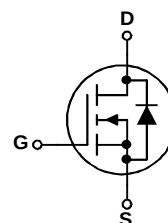
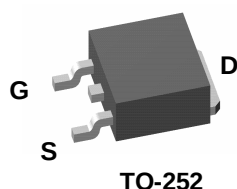
This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low $R_{DS(ON)}$ and fast switching speed. extremely low $R_{DS(ON)}$ in a small package.

Applications

- DC/DC converter
- Motor Drives

Features

- 78 A, 30 V $R_{DS(ON)} = 7.5 \text{ m}\Omega @ V_{GS} = 10 \text{ V}$
 $R_{DS(ON)} = 8.5 \text{ m}\Omega @ V_{GS} = 4.5 \text{ V}$
- Low gate charge
- Fast Switching
- High performance trench technology for extremely low $R_{DS(ON)}$



Absolute Maximum Ratings T_A=25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V _{DSS}	Drain-Source Voltage	30	V
V _{GSS}	Gate-Source Voltage	±16	V
I _D	Drain Current – Continuous (Note 3)	78	A
	– Pulsed (Note 1a)	100	
P _D	Power Dissipation for Single Operation (Note 1)	83	W
	(Note 1a)	3.8	
	(Note 1b)	1.6	
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to +175	°C

Thermal Characteristics

R _{θJC}	Thermal Resistance, Junction-to-Case (Note 1)	1.8	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient (Note 1a)	40	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient (Note 1b)	96	°C/W

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity
FDD6676	FDD6676	13"	12mm	2500 units

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Drain-Source Avalanche Ratings (Note 2)						
W _{DSS}	Drain-Source Avalanche Energy	Single Pulse, V _{DD} = 15 V, I _D =21A			370	mJ
I _{AR}	Drain-Source Avalanche Current				21	A
Off Characteristics						
BV _{DSS}	Drain–Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I _D = 250 μA,Referenced to 25°C		24		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 24 V, V _{GS} = 0 V			1	μA
I _{GSSF}	Gate–Body Leakage, Forward	V _{GS} = 16 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate–Body Leakage, Reverse	V _{GS} = −16 V V _{DS} = 0 V			−100	nA
On Characteristics (Note 2)						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	1	1.5	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	I _D = 250 μA,Referenced to 25°C		-5		mV/°C
R _{DS(on)}	Static Drain–Source On–Resistance	V _{GS} = 10 V, I _D = 16.8 A V _{GS} = 4.5 V, I _D = 15.8 A V _{GS} = 10 V, I _D = 16.8 A,T _J =125°C		4.8 5.4 7.3	7.5 8.5 10.5	mΩ
I _{D(on)}	On–State Drain Current	V _{GS} = 10 V, V _{DS} = 5 V	50			A
g _{FS}	Forward Transconductance	V _{DS} = 5 V, I _D = 16.8 A		80		S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 15 V, V _{GS} = 0 V, f = 1.0 MHz		5103		pF
C _{oss}	Output Capacitance			836		pF
C _{rss}	Reverse Transfer Capacitance			361		pF
Switching Characteristics (Note 2)						
t _{d(on)}	Turn–On Delay Time	V _{DD} = 15 V, I _D = 1 A, V _{GS} = 10 V, R _{GEN} = 6 Ω		15	27	ns
t _r	Turn–On Rise Time			9	18	ns
t _{d(off)}	Turn–Off Delay Time			87	139	ns
t _f	Turn–Off Fall Time			40	64	ns
Q _g	Total Gate Charge	V _{DS} = 15V, I _D = 16.8 A, V _{GS} = 5 V		45	63	nC
Q _{gs}	Gate–Source Charge			13		nC
Q _{gd}	Gate–Drain Charge			12		nC

Electrical Characteristics (continued)

$T_A = 25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Drain–Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain–Source Diode Forward Current				3.2	A
V_{SD}	Drain–Source Diode Forward Voltage	$V_{GS} = 0\text{ V}, I_S = 3.2\text{ A}$ (Note 2)		0.7	1.2	V

Notes:

- $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design.



a) $R_{\theta JA} = 40^{\circ}\text{C/W}$ when mounted on a 1 in² pad of 2 oz copper



b) $R_{\theta JA} = 96^{\circ}\text{C/W}$ when mounted on a minimum pad.

Scale 1 : 1 on letter size paper

- Pulse Test: Pulse Width < 300μs, Duty Cycle < 2.0%

- Maximum current is calculated as:
$$\sqrt{\frac{P_D}{R_{DS(ON)}}}$$

where P_D is maximum power dissipation at $T_C = 25^{\circ}\text{C}$ and $R_{DS(on)}$ is at $T_{J(max)}$ and $V_{GS} = 10\text{V}$. Package current limitation is 21A

Typical Characteristics

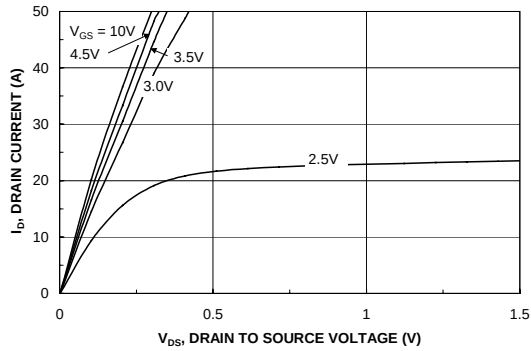


Figure 1. On-Region Characteristics

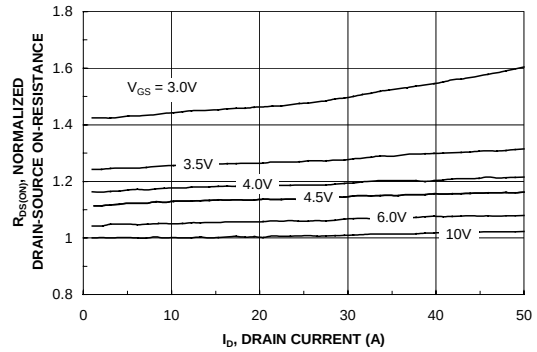


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage

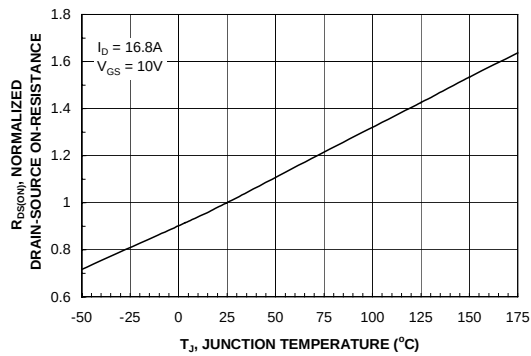


Figure 3. On-Resistance Variation with Temperature

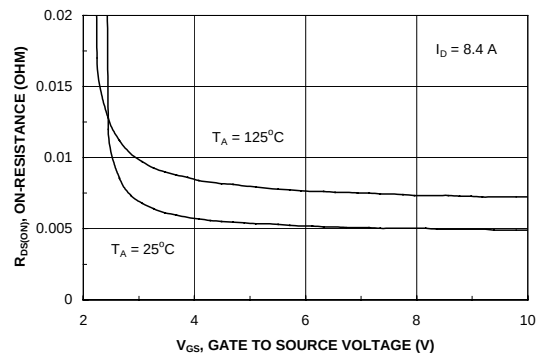


Figure 4. On-Resistance Variation with Gate-to-Source Voltage

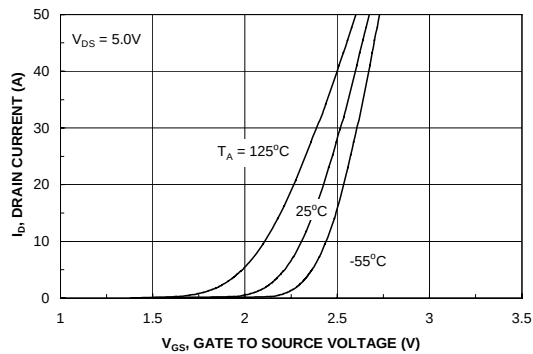


Figure 5. Transfer Characteristics

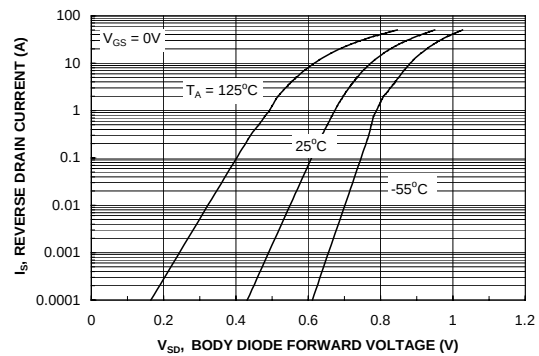


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature

Typical Characteristics

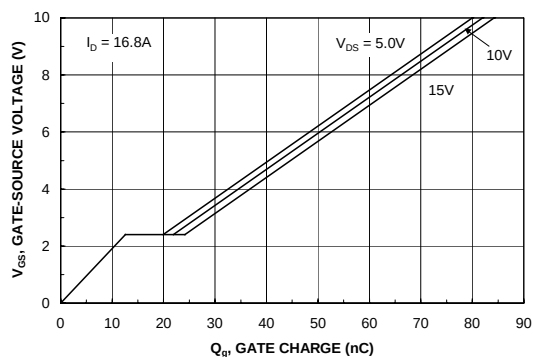


Figure 7. Gate Charge Characteristics

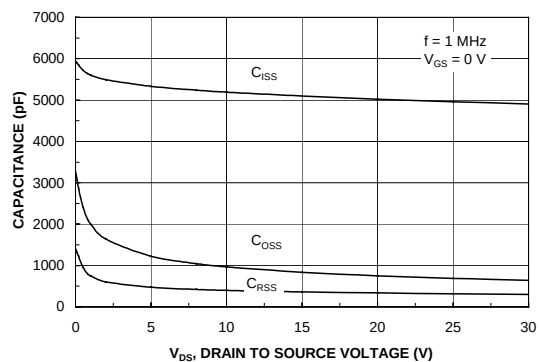


Figure 8. Capacitance Characteristics

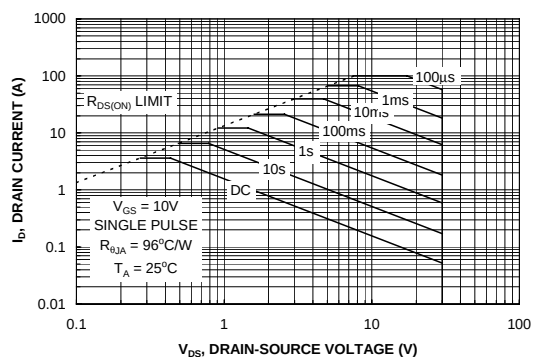


Figure 9. Maximum Safe Operating Area

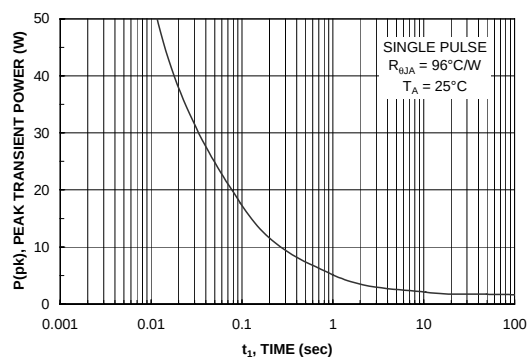


Figure 10. Single Pulse Maximum Power Dissipation

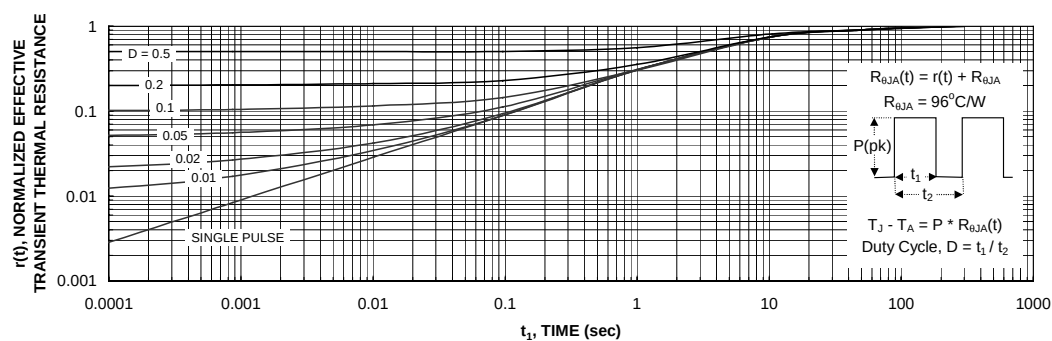


Figure 11. Transient Thermal Response Curve

Thermal characterization performed using the conditions described in Note 1b.
Transient thermal response will change depending on the circuit board design.

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