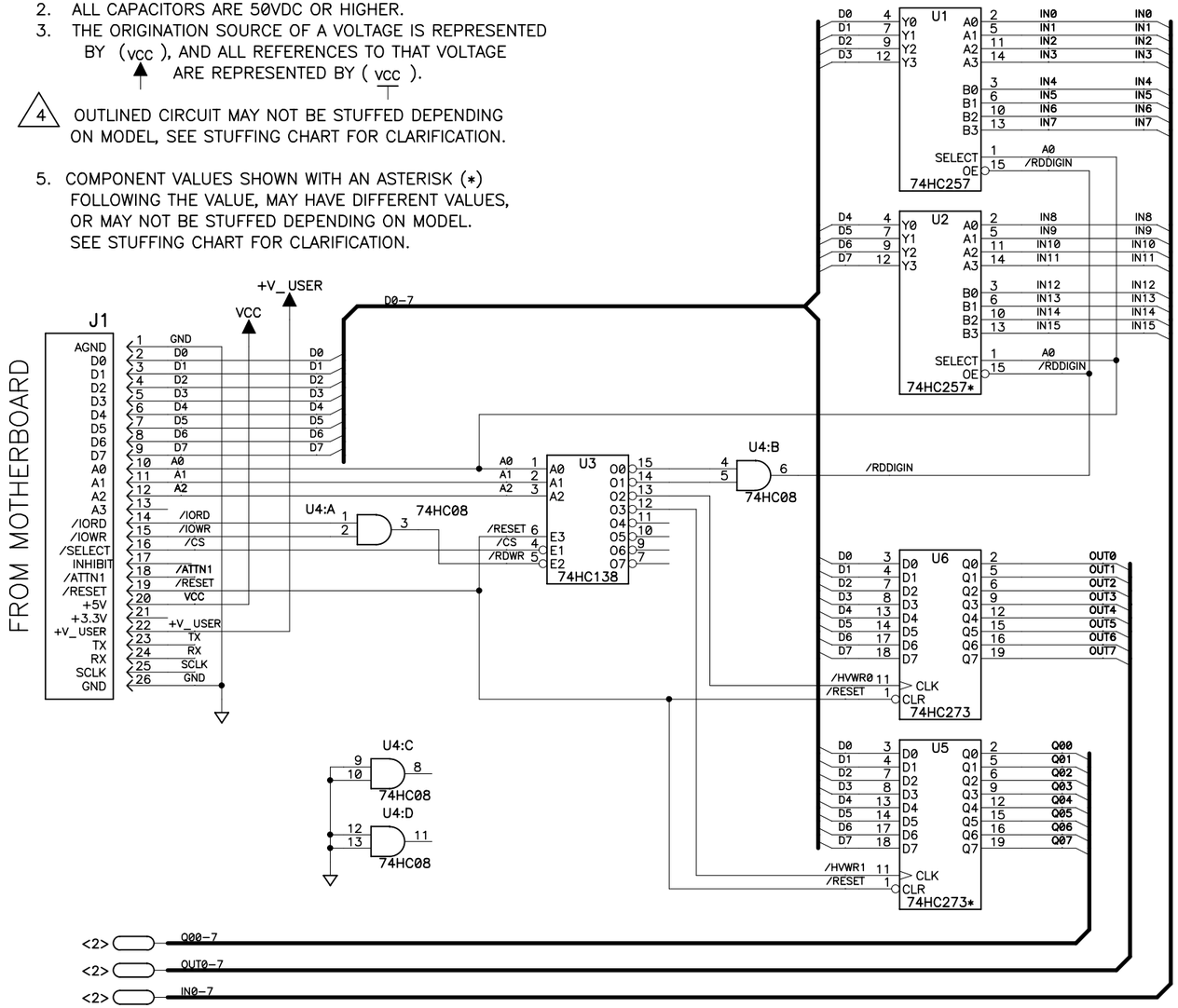
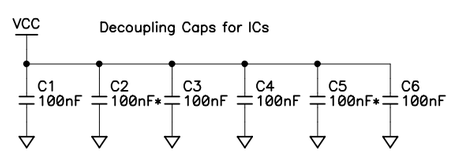
NOTES: UNLESS OTHERWISE SPECIFIED;

1. ALL RESISTOR VALUES ARE IN OHMS, 1/10W, 5%
2. ALL CAPACITORS ARE 50VDC OR HIGHER.
3. THE ORIGINATION SOURCE OF A VOLTAGE IS REPRESENTED BY (VCC), AND ALL REFERENCES TO THAT VOLTAGE ARE REPRESENTED BY (VCC).

△4 OUTLINED CIRCUIT MAY NOT BE STUFFED DEPENDING ON MODEL, SEE STUFFING CHART FOR CLARIFICATION.

5. COMPONENT VALUES SHOWN WITH AN ASTERISK (*) FOLLOWING THE VALUE, MAY HAVE DIFFERENT VALUES, OR MAY NOT BE STUFFED DEPENDING ON MODEL. SEE STUFFING CHART FOR CLARIFICATION.





REVISION HISTORY			REVISION APPROVAL			
REV	ECO	DESCRIPTION	PROJECT ENGINEER	APPROVAL DATE	DOCUMENT CONTROL	APPROVAL DATE
A	E11217	INITIAL RELEASE, TRACKS A/W @ REV-A	DM	11/20/00	KIS	11/17/01
B	E11564	CHANGED PE7 TO OUTPUT INHIBIT	DM	28JUN01	KIS	7/17/01

TABLE A

REF DES	DEVICE	DEVICE VOLTAGE INFORMATION					DEVICE: FILTER CAP
		AGND	GND	VCC	-5V	NO CONNECTS	REF DES(s)
U1	74HC257		8	16			C1
U2	74HC257		8	16			C2
U3	74HC138		8	16			C3
U4	74HC08		7	14			C4
U5	74HC273		10	20			C5
U6	74HC273		10	20			C6

STUFFING TABLE

	CIRCUIT		PART	16 INPUT 8 OUTPUT MODEL SR9200	8 INPUT 16 OUTPUT MODEL SR9210	8 INPUT 8 OUTPUT MODEL SR9220
	FPWR SELECT		JP1	ZERO ohm ACROSS PINS 2-3	ZERO ohm ACROSS PINS 2-3	ZERO ohm ACROSS PINS 2-3
BANK 0	INPUT LEVEL SELECT		JP2	ZERO ohm ACROSS PINS 1-2	ZERO ohm ACROSS PINS 1-2	ZERO ohm ACROSS PINS 1-2
BANK 2	HIGH VOLTAGE SINKING DRIVERS	CURRENT DRIVER	R36-43 C28-35 D28-35 Q28-35	NOT INSTALLED	INSTALLED	NOT INSTALLED
		DRIVER CHIP	U5 C5	NOT INSTALLED	INSTALLED	NOT INSTALLED
	DIGITAL INPUTS	INPUT CONDITIONING	R60-67 C44-51	INSTALLED	NOT INSTALLED	NOT INSTALLED
		DRIVER CHIP	U2 C2	INSTALLED	NOT INSTALLED	NOT INSTALLED
	DIGITAL I/O LEVEL		JP3 1-2 R68-75	INSTALLED	NOT INSTALLED	NOT INSTALLED
BANK 1	DIGITAL OUTPUT LEVEL		R28-35	NOT INSTALLED	NOT INSTALLED	NOT INSTALLED

APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING THIS DOCUMENT:	

DRAWING CONTENT:	
DRAWN BY: (INITIAL RELEASE) RM	29JUL99
REVISED BY: DM	26JUN01
APPROVALS: INITIAL RELEASE	
PROJECT ENGINEER: Darren Musgrove	7/20/01
ENGINEERING MANAGER: R.Matthews	7/20/01
SIGNATURES	DATE

TITLE
SCHEMATIC DIAGRAM SR9200 SERIES DIGITAL I/O BOARD

Z-WORLD
2900 SPAFFORD ST.
DAVIS, CA 95616
530 - 757 - 4616

SIZE B | DWG NO. 090-0101 | SCALE NONE | RELEASE DATE | SHEET 1 OF 2

COPYRIGHT 2001, Z-WORLD, INC.

OUT0–7
Q00–7
IN0–7

USER INTERFACE

J2

GND	1	GND	
DIGIN0	2	DIGIN0	DIGIN0
DIGIN1	3	DIGIN1	DIGIN1
DIGIN2	4	DIGIN2	DIGIN2
DIGIN3	5	DIGIN3	DIGIN3
GND	6	GND	
K	7	K	
DIGIN4	8	DIGIN4	DIGIN4
DIGIN5	9	DIGIN5	DIGIN5
DIGIN6	10	DIGIN6	DIGIN6
DIGIN7	11	DIGIN7	DIGIN7
GND	12	GND	
I/O8	13	I/O8	I/O8
I/O9	14	I/O9	I/O9
I/O10	15	I/O10	I/O10
I/O11	16	I/O11	I/O11
GND	17	GND	
I/O12	18	I/O12	I/O12
I/O13	19	I/O13	I/O13
I/O14	20	I/O14	I/O14
I/O15	21	I/O15	I/O15
HVOUT0	22	HVOUT0	HVOUT0
HVOUT1	23	HVOUT1	HVOUT1
HVOUT2	24	HVOUT2	HVOUT2
GND	25	GND	
HVOUT3	26	HVOUT3	HVOUT3
HVOUT4	27	HVOUT4	HVOUT4
K	28	K	
HVOUT5	29	HVOUT5	HVOUT5
HVOUT6	30	HVOUT6	HVOUT6
HVOUT7	31	HVOUT7	HVOUT7
	32		
GND	33	GND	
	34		

Install a 0 Ohm Resistor on
JP1:1–2 for K (Customer provides Power Source)
JP1:2–3 for +V_USER (Power from Z–Bus)

+V_USER
JP1*
FPWR
K

BANK 0
DIGITAL INPUTS

BANK 2
FACTORY CONFIGURABLE
DIGITAL INPUTS
4

Install 0 Ohm Resistor on
JP2:1–2 for Pull–Ups
JP2:2–3 for Pull–Downs

VCC
JP2*

IN0	R52	22k	C36	10nF
IN1	R53	22k	C37	10nF
IN2	R54	22k	C38	10nF
IN3	R55	22k	C39	10nF
IN4	R56	22k	C40	10nF
IN5	R57	22k	C41	10nF
IN6	R58	22k	C42	10nF
IN7	R59	22k	C43	10nF

DIGIN0	R44	
DIGIN1	R45	
DIGIN2	R46	
DIGIN3	R47	
DIGIN4	R48	
DIGIN5	R49	
DIGIN6	R50	
DIGIN7	R51	10k

IN8	R67	I/O8	C51	10nF
IN9	R66	I/O9	C50	10nF
IN10	R65	I/O10	C49	10nF
IN11	R64	I/O11	C48	10nF
IN12	R63	I/O12	C47	10nF
IN13	R62	I/O13	C46	10nF
IN14	R61	I/O14	C45	10nF
IN15	R60	I/O15	C44	10nF

BANK 2
FACTORY CONFIGURABLE
HIGH VOLTAGE SINKING DRIVERS
4

Q00	R43	536 1%	Q35 MMBT4401	D35 914	C35 100nF
Q01	R42	536 1%	Q34 MMBT4401	D34 914	C34 100nF
Q02	R41	536 1%	Q33 MMBT4401	D33 914	C33 100nF
Q03	R40	536 1%	Q32 MMBT4401	D32 914	C32 100nF
Q04	R39	536 1%	Q31 MMBT4401	D31 914	C31 100nF
Q05	R38	536 1%	Q30 MMBT4401	D30 914	C30 100nF
Q06	R37	536 1%	Q29 MMBT4401	D29 914	C29 100nF
Q07	R36	536 1%	Q28 MMBT4401	D28 914	C28 100nF

DIGITAL I/O
LEVEL
4

Install 0 Ohm Resistor on
JP3:1–2 for Pull–Ups
JP3:2–3 for Pull–Downs

VCC
JP3

I/O8	R75	
I/O9	R74	
I/O10	R73	
I/O11	R72	
I/O12	R71	
I/O13	R70	
I/O14	R69	
I/O15	R68	10k

BANK 1
HIGH VOLTAGE SINKING DRIVERS

OUT0	R27	536 1%	Q27 MMBT4401	D27 914	C27 100nF
OUT1	R26	536 1%	Q26 MMBT4401	D26 914	C26 100nF
OUT2	R25	536 1%	Q25 MMBT4401	D25 914	C25 100nF
OUT3	R24	536 1%	Q24 MMBT4401	D24 914	C24 100nF
OUT4	R23	536 1%	Q23 MMBT4401	D23 914	C23 100nF
OUT5	R22	536 1%	Q22 MMBT4401	D22 914	C22 100nF
OUT6	R21	536 1%	Q21 MMBT4401	D21 914	C21 100nF
OUT7	R20	536 1%	Q20 MMBT4401	D20 914	C20 100nF

DIGITAL OUTPUT
LEVELS
4

VCC

HVOUT0	R35	10k*
HVOUT1	R34	10k*
HVOUT2	R33	10k*
HVOUT3	R32	10k*
HVOUT4	R31	10k*
HVOUT5	R30	10k*
HVOUT6	R29	10k*
HVOUT7	R28	10k*

DIGIIN0–7
I/O8–15
HVOUT0–7

26JUN01

COPYRIGHT 2001, Z–WORLD, INC.

SIZE	DWG NO.	
B	090–0101	
SCALE NONE	REV LTR B	SHEET 2 OF 2

ООО «НИОКРсистемс» - это оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов. Реализуемая нашей компанией продукция насчитывает более полумиллиона наименований.

Благодаря этому наша компания предлагает к поставке практически не ограниченный ассортимент компонентов как оптовыми, мелкооптовыми партиями, так и в розницу.

Благодаря развитой сети поставщиков, помогаем в поиске и приобретении экзотичных или снятых с производства компонентов.

Наша компания это:

- Гарантия качества поставляемой продукции
- Широкий ассортимент
- Минимальные сроки поставок
- Техническая поддержка
- Подбор комплектации
- Индивидуальный подход
- Гибкое ценообразование
- Работаем по 275 ФЗ

ООО «НИОКР СИТЕМС»
Москва, 125362, Россия, ул. Вишневая, д. 9, к. 1, Блок 104 а и 104 в

Телефон: 8 (495) 268-14-82
Email: n@nsistems.ru

ИНН: 7735154786
ОГРН: 1167746717709